GOVERNMENT GIRLS POLYTECHNIC BILASPUR

DEPARTMENT OF COMPUTER SCIENCE

LESSON PLAN

Session : Jan-June 2025

Session start date as per University Calendar-20/01/2025
Course Name : COMPUTER ORGANIZATION & MICROPROCESSOR
Subject Code : 2022473(022)
Name of Subject teacher : SUMEET KUMAR DEWANGAN
Lecture plus Tutorial/Week : 3

LESSON PLAN		
SESSION : Jan-June 2025		
Discipline : Computer Science Engineering		**Semester : 4th**
Course Outcomes.	**Topics/Subtopics to be covered under this unit**	**No. of periods planned**
CO-1 Identify the Structure of 8085 Microprocessor and its application	Introduction to Microprocessor , List of terms used in Microprocessor & Features	1
	Architectural Block Diagram of Microprocessor 8085	1
	Microprocessor 8085 functional units Multiplexed Address/Data Bus, Accumulator	1
	Arithmetic & Logic Unit(ALU), General Purpose Register (GPR)	1
	Program Counter, Stack Pointer, Flag Register	1
	Microprocessor 8085 Pin Configuration and pin functions	3
CO-2 Execute programs on Various Instrcutions of 8085	Introduction to Instruction and Instruction cycle	1
	Opcode Fetch Cycle	1
	Execution Cycle, Timing Diagram Opcode Fetch Cycle	1
	Timing Diagram Memory Read Cycle	1
	Timing Diagram Memory Write MOV R1,R2	1
	Timing Diagram of MOV R,M & MOV M,R	1
	Addressing modes of 8085, Immediate addressing mode & Regsiter addresing mode.	1
	Direct and Indirect addressing mode	1
	Classification of 8085 Instruction set and Data Transfer Instructions	1
	Data Transfer instructions, Arithmetic and logic Instructions	1
	Shift and rotate Instructions	1
	Program Control Instructions	1
	Branching Instructions, Stack and machine control instructions	1

CO-3 Develop various programs on assembly language programming of 8085	Introduction to machine and assembly language	1
	Assemblers first pass, Introduction to assembly language programming	1
	Arithmetic operation of two numbers	2
	Swapping of two numbers stored in register and memory	1
	program for PUSH & POP	2
	8085 Interrupt Struture and Hardware Interrupts	2
	Software Interrupts	1
	Architectural Block Diagram of Microprocessor 8086	2
CO-4 Interface various Input and Output devices to 8085 Microprocessor	Introduction to Input-Output Interface	1
	I/O Bus and interface Module , I/O vs Memory BUs	1
	Isolated vs Memory Mapped I/O	1
	Asynchronus Data Transfer Strobe control & Handshaking	1
	Modes of data Transfer Programmed I/O, Interrupt Initiated I/O & Software considerations	1
	Direct Memory Access and DMA Controller, DMA Transfer Process	2
	Introduction to input output Processor	1
	CPU-IOP Communication	1
CO-5 Compare merit and demerits of various types of Memory	Memory Hierarchy	1
	Auxiliary Memory ,Magentic Tape and Disk	1
	Associative Mapping, Direct and set associative mapping	2
	Virtual Memory (Address Sapce and Memory space)	1
	Address maping using table	1
	Address maping using Pages and Memory mnagement Hardware	1
Total Class		**49**

Class room Instruction End date:-

Subject Teacher : Sumeet Kumar Dewangan